

University of Pretoria Yearbook 2025

BEngHons Computer Engineering (12240214)

Department Electrical, Electronic and Computer Engineering

Minimum duration of

study

1 year

Total credits 128

NQF level 08

Programme information

Refer also to G16-G29.

The curriculum is determined in consultation with the relevant heads of departments. A student is required to pass modules to the value of at least 128 credits.

The degree is awarded on the basis of examinations only.

Admission requirements

- 1. BEng degree awarded by the University of Pretoria **or** equivalent degree **or** relevant four-year bachelor's degree in engineering that the Engineering Council of South Africa (ECSA) regards as acceptable for registration as a candidate engineer and for eventual registration as a professional engineer
- 2. An entrance examination may be required
- 3. Comprehensive intellectual CV

Other programme-specific information

Students may take modules to the value of 32 credits from other fields of specialisation or from other departments, with approval of the Coordinator: Postgraduate Studies.

Examinations and pass requirements

Refer also to G18 and G26.

- i. The examination in each module for which a student is registered, takes place during the normal examination period after the conclusion of lectures (i.e. October/November or May/June).
- ii. G18(1) applies with the understanding that under exceptional circumstances an extension of a maximum of three years may be approved: provided that the Dean, on reccommendation of the relevant head of department, may approve a stipulated limited extension of this period.
- iii. A student must obtain at least 50% in an examination for each module where no semester or year mark is required. A module may only be repeated once.
- iv. In modules where semester or year marks are awarded, a minimum examination mark of 40% and a final mark of 50% is required.



v. No supplementary or special examinations are granted at postgraduate level.

Pass with distinction

A student passes with distinction if he or she obtains a weighted average of at least 75% (not rounded) in the first 128 credits for which he or she has registered (excluding modules which were discontinued timeously). The degree is not awarded with distinction if a student fails any one module (excluding modules which were discontinued timeously). The degree must be completed within the prescribed study period.



Curriculum: Final year

Minimum credits: 128

EIN 732 is a compulsory module. With permission from the department it may be substituted with:

EPT 732 OR

EPT 733

EIN 732 is 'n verpligte module. Met toestemming van die departement mag dit vervang word met:

EPT 732 OF

EPT 733

Core modules

Intelligent systems 732 (EAI 732)

Module credits	32.00
NQF Level	08
Prerequisites	No prerequisites.
Contact time	10 lectures per week
Language of tuition	Module is presented in English
Department	Electrical, Electronic and Computer Engineering

Period of presentation Semester 1

Module content

This module provides the theoretical background necessary to understand, research and develop real-world software and hardware systems that incorporate and exhibit intelligent behaviour. The module incorporates advanced theory from fields such as Artificial Intelligence, Computational Intelligence, Machine Learning, Pattern Recognition and Signal Processing. Core topics of the module include: Bayesian Theory, Neural Networks, Kernel Methods, Graphic Models, and Numerical Bayesian Methods.

Advanced topics in intelligent systems 733 (EAI 733)

Module credits	32.00
NQF Level	08
Prerequisites	EAI 732
Contact time	10 lectures per week
Language of tuition	Module is presented in English
Department	Electrical, Electronic and Computer Engineering
Period of presentation	Semester 1 or Semester 2



Module content

The aim of the module is to augment the general background provided by the EAI 732 module with the specific theoretical background required for MEng. The module will, depending on the intended research field of the student, incorporate advanced theory from fields such as: Digital Image Processing, Computer and Robotic Vision, Probabilistic Robotics, Data Fusion, Hardware and Software Parallel Processing, Real-Time and Reactive Systems.

Introduction to research 732 (EIN 732)

Module credits	32.00
NQF Level	08
Prerequisites	No prerequisites.
Contact time	16 contact hours per semester
Language of tuition	Module is presented in English
Department	Electrical, Electronic and Computer Engineering

Period of presentation Semester 1 or Semester 2

Module content

The aim of this module is to teach students to critically evaluate research literature, including conference papers and journal articles, in order to determine the current state of knowledge in a particular specialist area. It will also provide students with the principles of research to enable them to conduct research and prepare an original project in their particular specialist area.

Wireless sensor networks 732 (EKS 732)

Module credits	32.00
NQF Level	08
Prerequisites	ERN 780
Contact time	32 contact hours per semester
Language of tuition	Module is presented in English
Department	Electrical, Electronic and Computer Engineering
Period of presentation	Semester 1 or Semester 2

Module content

WSN consist of individual nodes interacting with their environment by sensing or controlling physical parameters; these nodes have to collaborate (using wireless communication) to fulfil their tasks. The course can be structured in two parts: architectures covering single node and network architectures, and communication protocols focusing on algorithms and protocols relevant to wireless sensor networks. The latter include the physical layer, MAC protocols, link-layer, naming and addressing, time synchronisation, localisation and positioning, topology control, routing protocols, data-centric and content-based networking, transport layer an QoS, and advanced application support (e.g. security).

^{*}This is a compulsory module.



Research project: Theory 732 (EPT 732)

Module credits 32.00

NQF Level 08

Prerequisites No prerequisites.

Contact time 10 lectures per week

Language of tuition Module is presented in English

Department Electrical, Electronic and Computer Engineering

Period of presentation Semester 1 or Semester 2

Module content

This module will cover the essential theoretical background of the student's proposed M Eng topic and include inter alia the following:

- (i) Field definition and descriptions
- (ii) In-depth study into background and theory relevant to the problem to be addressed
- (iii) Problem definition and description
- (iv) Mathematical simulations of the problem

Research project: Design and laboratory 733 (EPT 733)

Module credits 32.00

NQF Level 08

Prerequisites No prerequisites.

Contact time 10 lectures per week

Language of tuition Module is presented in English

Department Electrical, Electronic and Computer Engineering

Period of presentation Semester 1 or Semester 2

Module content

This module will include extensive laboratory experiments to test the principles and possible solutions of the proposed M Eng research project and will include inter alia the following. These will include hardware and/or software experiments:

- (i) Introduction to instrumentation and measuring techniques in general and specifically as applied in the field of research.
- (ii) Structured laboratory work to introduce the specific problem investigated for the research undertaken.
- (iii) Structured laboratory work to test the proposed solution for the problem addressed.
- (iv) Confirmation experiments.

Digital communications 732 (ETD 732)

Module credits 32.00

NQF Level 08

Prerequisites No prerequisites.



Contact time 32 contact hours per semester

Language of tuition Module is presented in English

Department Electrical, Electronic and Computer Engineering

Period of presentation Semester 1

Module content

Digital Communications ETD 732 is a first semester graduate course in Electronic Engineering, presented by the Signal Processing and Telecommunications Group, in collaboration with the Centre for Radio and Digital Communication (CRDC). The content of the course is as follows: Introduction to digital communications, digital communications applications and services. Review of: probability and stochastic processes, source coding, characterisation of communication signals and systems and optimum receivers for the AWGN channel. Advanced synchronisation systems: Carrier and symbol recovery. Shannon's channel capacity theorem and introduction to coding. Signal design for band-limited channels. Digital modulation techniques. Communication through band-limited linear filter channels. Introduction to adaptive equalisation. Spread spectrum signals for digital communications. Simulation of digital communication systems. Digital realisation of digital communication subsystems. Digital communication laboratory.

Telecommunication systems engineering 732 (ETT 732)

Module credits 32.00

NQF Level 08

Prerequisites No prerequisites.

Contact time 32 contact hours per semester

Language of tuition Module is presented in English

Department Electrical, Electronic and Computer Engineering

Period of presentation Semester 1 or Semester 2

Module content

Telecommunication systems engineering ETT 732 is a first semester graduate course in Electronic Engineering, presented by the Signals and Telecommunications Group. This module provides an Introduction to telecommunication concepts, telecommunication systems, virtual private networks (VPN), advanced intelligent networks (AIN), local number portability (LNP), computer-to-telephony integration (CTI), signalling system 7 (SS7), CTI technologies and application, ISDN, frame relay, ATM, ATM and frame relay internetworking, data over power lines, xDSL, microwave and radio-based systems, local multipoint distribution services (LMDS), specialized mobile radio (SMR), cellular communication, GSM, personal communication services (PCS), wireless data communication (Mobile IP), satellite communication (Networking, LEO), Sonet and SDH, wave division multiplexing (WDM), the internet (TCP/IP, VoIP, networking, management).

General Academic Regulations and Student Rules

The General Academic Regulations (G Regulations) and General Student Rules apply to all faculties and registered students of the University, as well as all prospective students who have accepted an offer of a place at



the University of Pretoria. On registering for a programme, the student bears the responsibility of ensuring that they familiarise themselves with the General Academic Regulations applicable to their registration, as well as the relevant faculty-specific and programme-specific regulations and information as stipulated in the relevant yearbook. Ignorance concerning these regulations will not be accepted as an excuse for any transgression, or basis for an exception to any of the aforementioned regulations. The G Regulations are updated annually and may be amended after the publication of this information.

Regulations, degree requirements and information

The faculty regulations, information on and requirements for the degrees published here are subject to change and may be amended after the publication of this information.

University of Pretoria Programme Qualification Mix (PQM) verification project

The higher education sector has undergone an extensive alignment to the Higher Education Qualification Sub-Framework (HEQSF) across all institutions in South Africa. In order to comply with the HEQSF, all institutions are legally required to participate in a national initiative led by regulatory bodies such as the Department of Higher Education and Training (DHET), the Council on Higher Education (CHE), and the South African Qualifications Authority (SAQA). The University of Pretoria is presently engaged in an ongoing effort to align its qualifications and programmes with the HEQSF criteria. Current and prospective students should take note that changes to UP qualification and programme names, may occur as a result of the HEQSF initiative. Students are advised to contact their faculties if they have any questions.